

FIG. 1 is a schematic diagram of a prior art device.

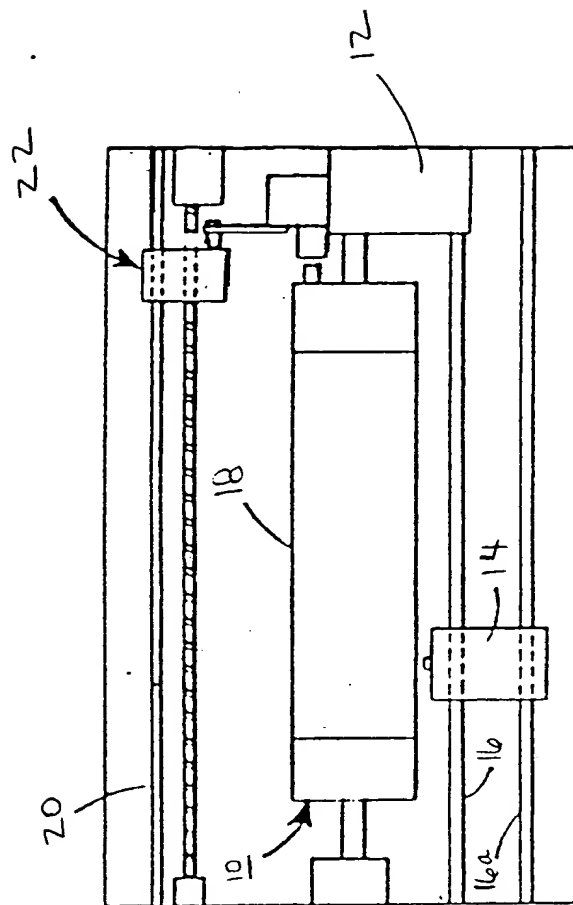


Figure 1  
Prior Art

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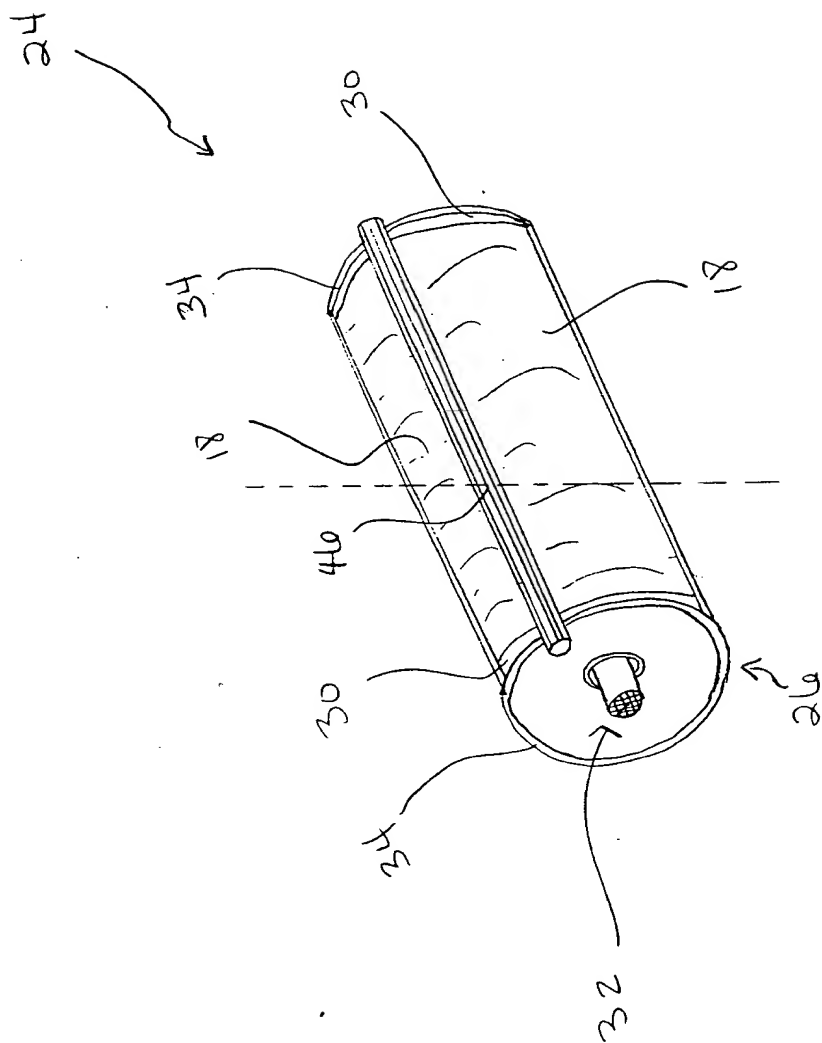


Figure 2

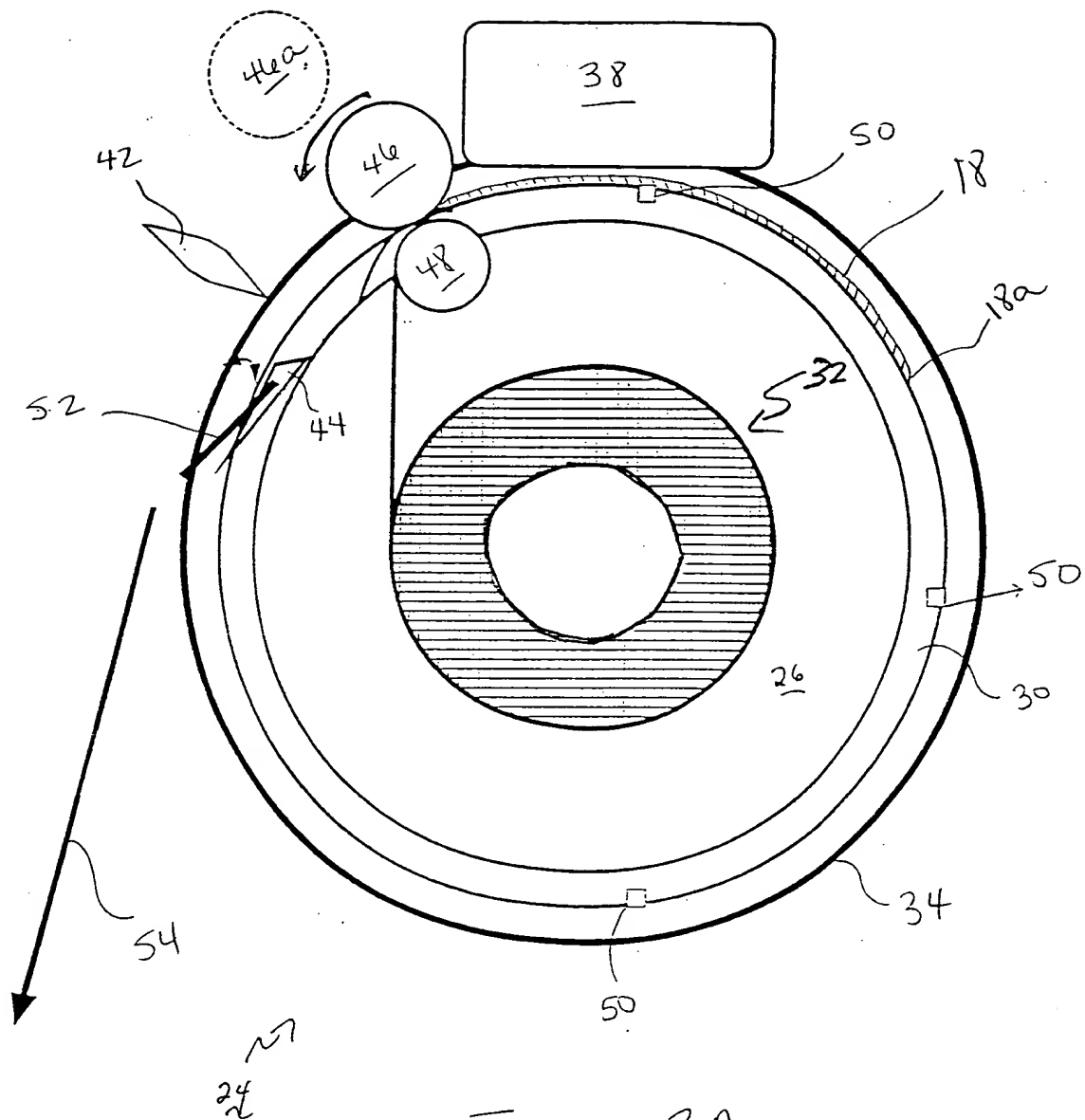


Figure 3A

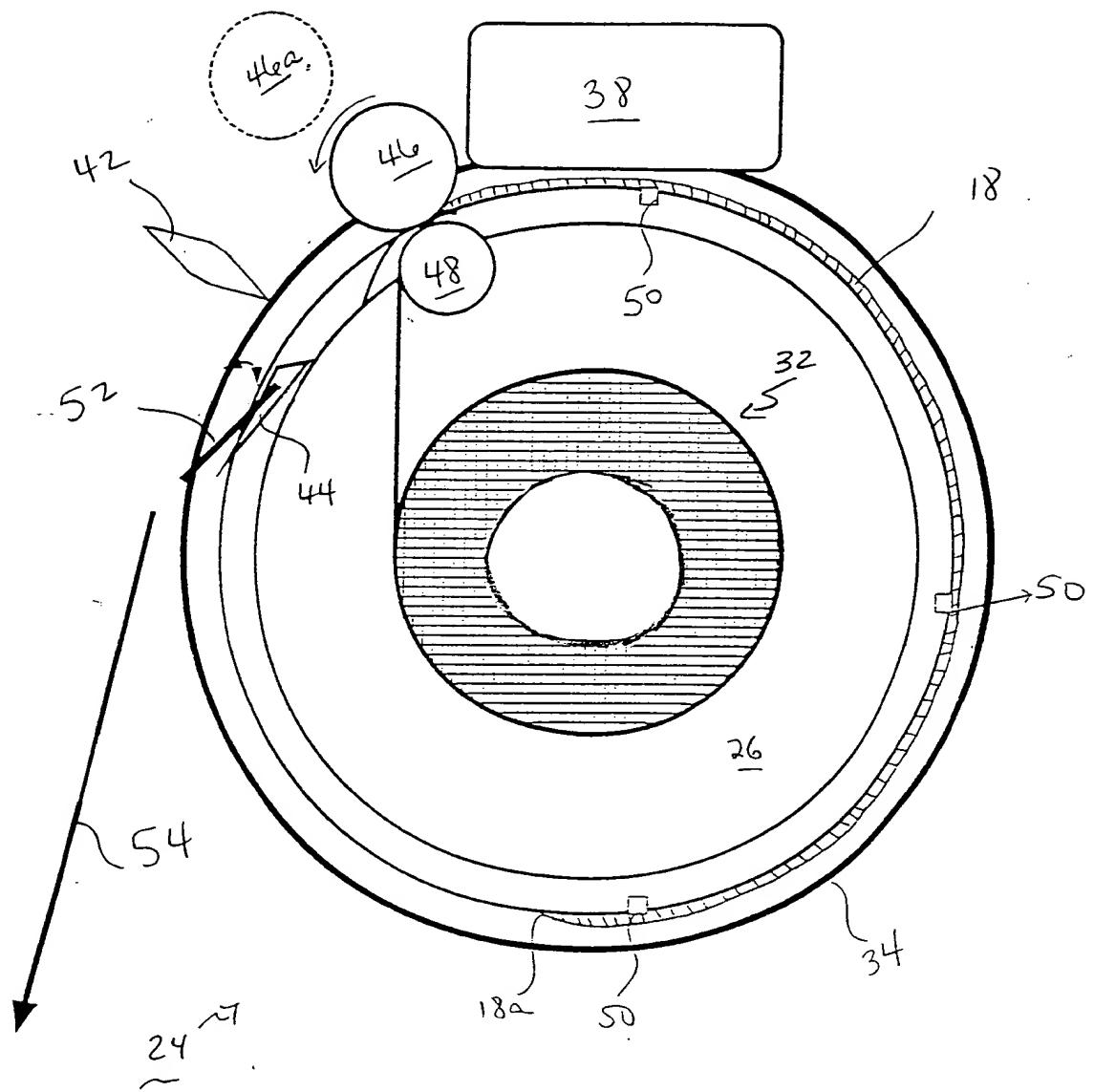


Figure 3B

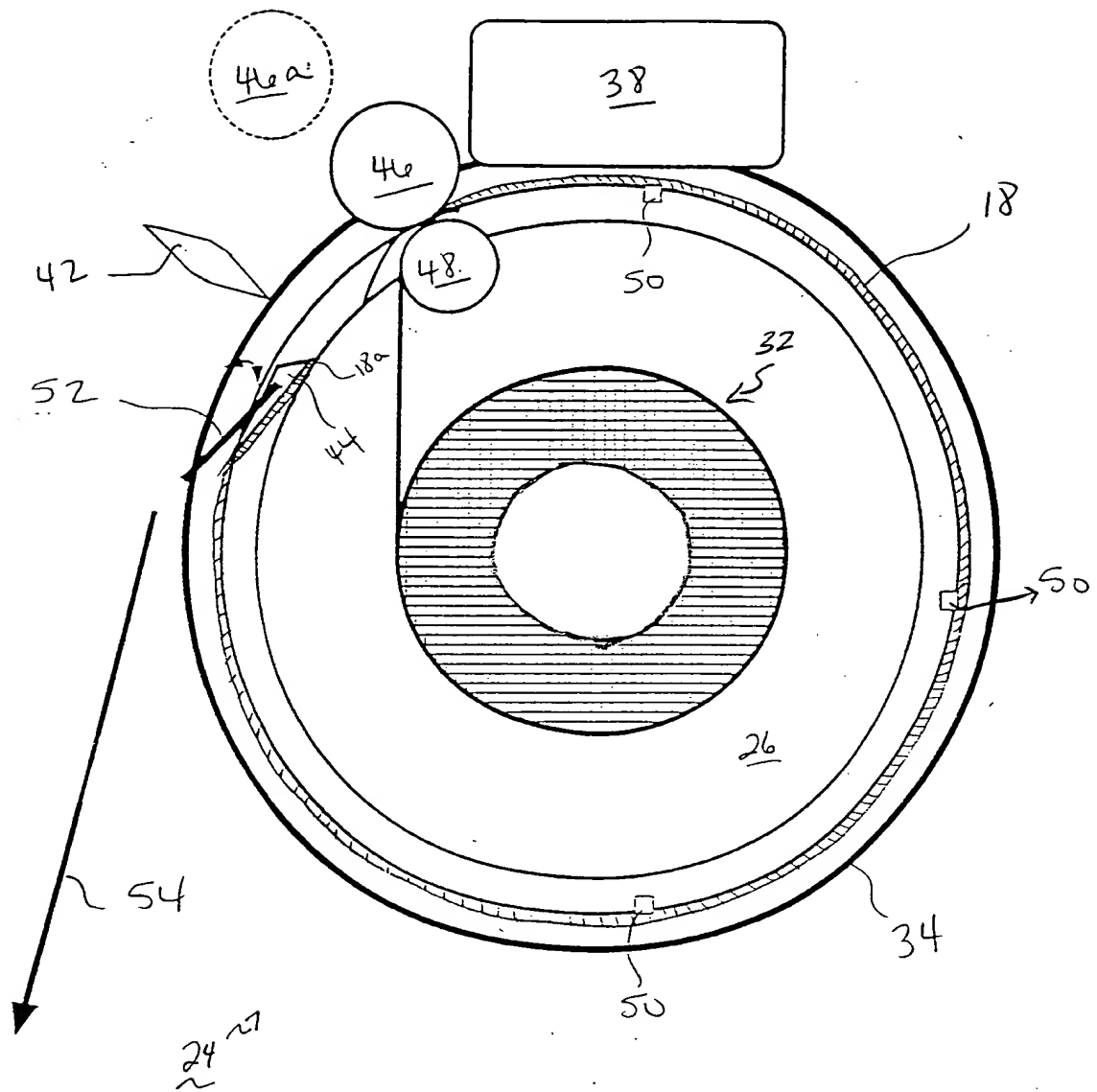


Figure 3C

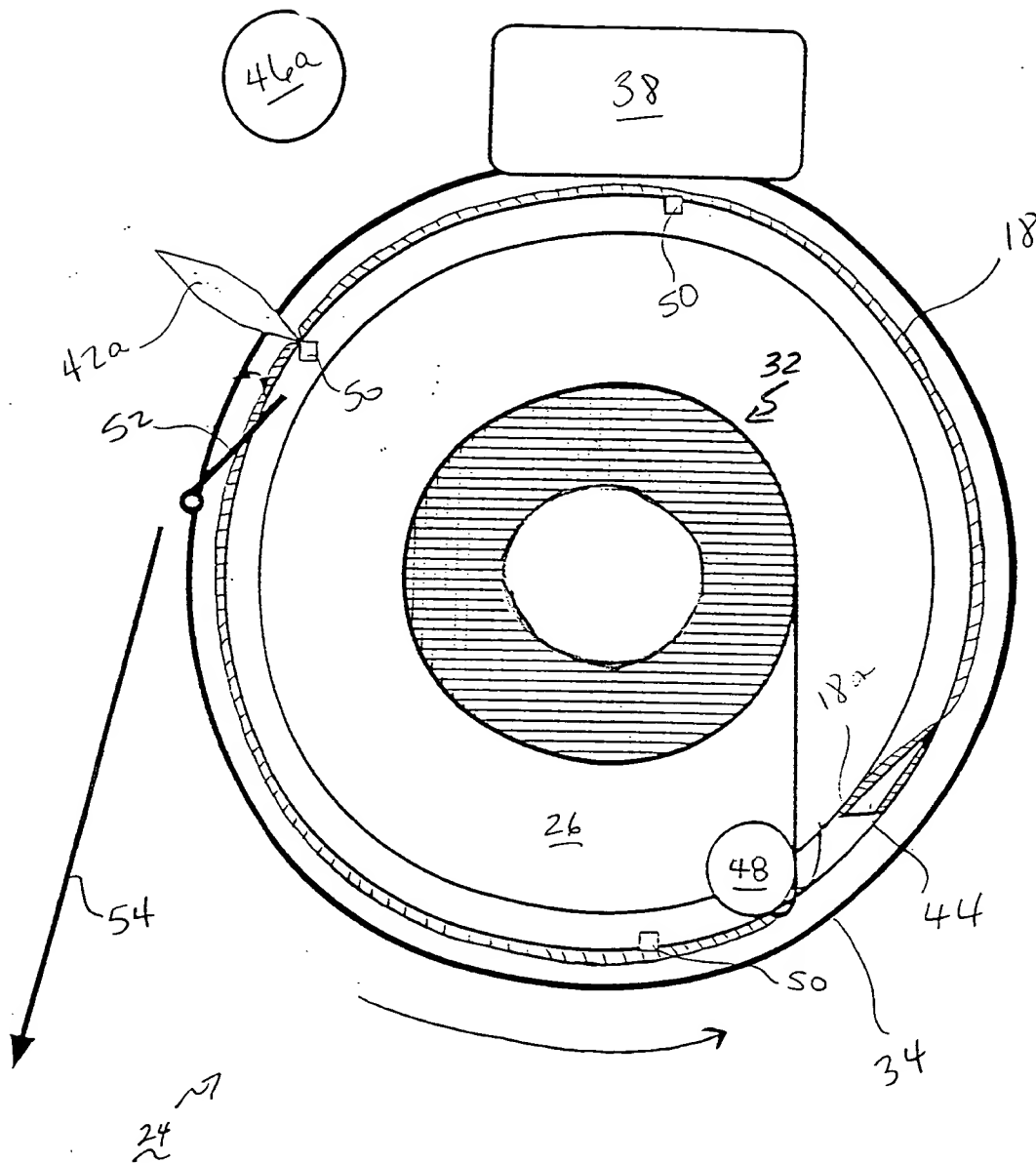


Figure 4A

POST PRINT STATION

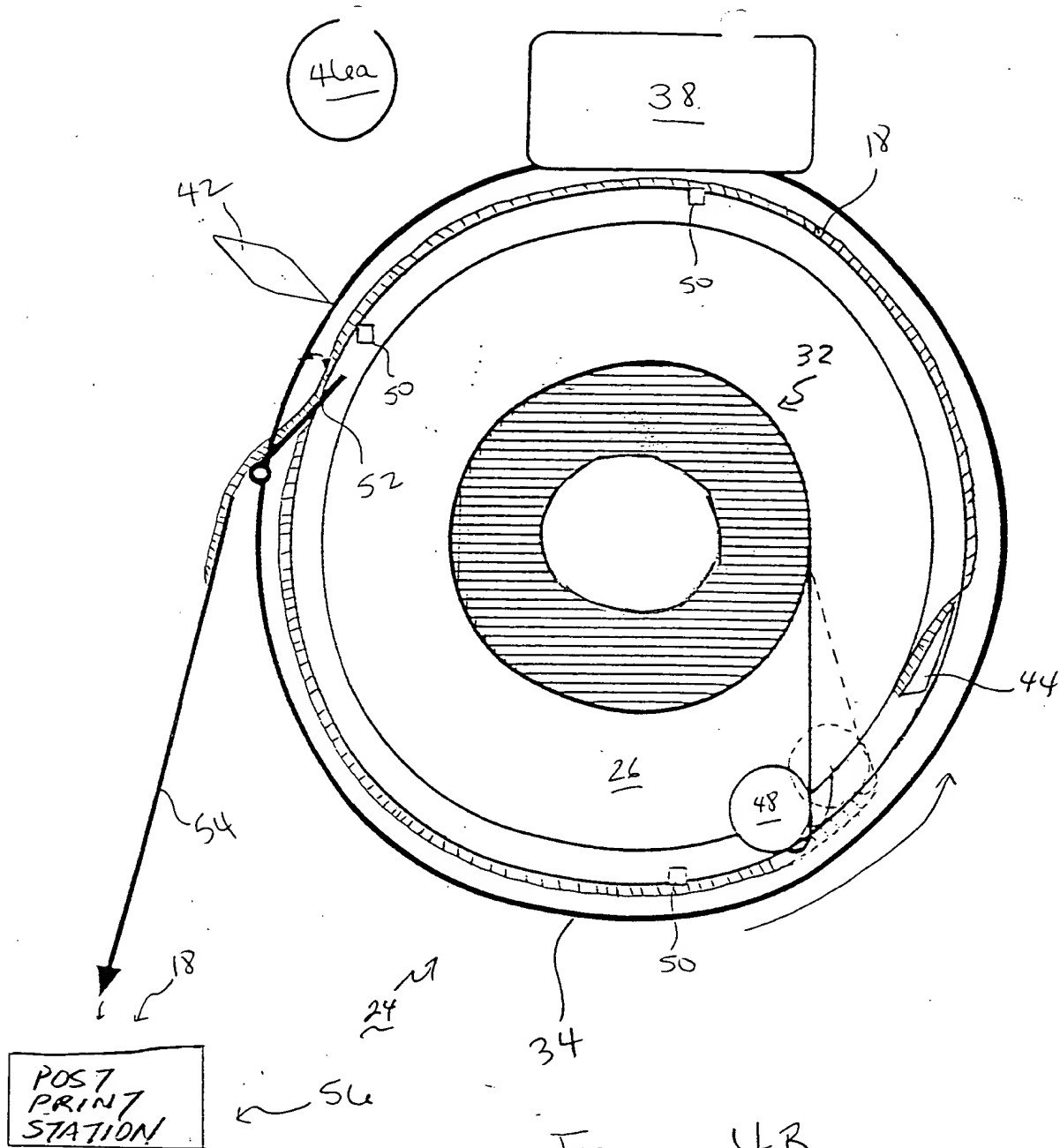


Figure 4B